## EE 435

## Lecture 28

## **Data Converter Characterization**

- Linearity Metrics
- Spectral Characterization

**Review From Last Lecture** 

## **INL-based ENOB**

Consider initially the continuous INL definition for an ADC where the INL of an ideal ADC is  $X_{LSB}/2$ 

Assume INL= $\theta X_{REF} = \upsilon X_{LSBR}$ 

where X<sub>LSBR</sub> is the LSB based upon the defined resolution

Define the effective LSB by  $\mathcal{X}_{\text{LSBEFF}} = \frac{\mathcal{X}_{\text{REF}}}{2^{n_{\text{EQ}}}}$ 

Thus

$$INL=\theta 2^{n_{EQ}} X_{LSBEFF}$$

Since an ideal ADC has an INL of  $X_{LSB}/2$ , express INL in terms of ideal ADC

$$\mathsf{INL} = \left[ \theta 2^{(\mathsf{n}_{\mathsf{EQ}}+1)} \right] \left( \frac{\mathsf{X}_{\mathsf{LSBEFF}}}{2} \right)$$

Setting term in [ ] to 1, can solve for  $n_{EQ}$  to obtain

ENOB = 
$$n_{EQ} = \log_2\left(\frac{1}{2\theta}\right) = n_R - 1 - \log_2(\nu)$$

where  $n_R$  is the defined resolution

#### Review From Last Lecture **INL-based ENOB** ENOB = $n_R$ -1-log<sub>2</sub>(v)

Consider an ADC with specified resolution of  $n_R$  and INL of v LSB

υ	ENOB
1/2	n
1	n-1
2	n-2
4	n-3
8	n-4
16	n-5

# **Differential Nonlinearity (DAC)**

#### Nonideal DAC



DNL(k) is the actual increment from code (k-1) to code k minus the ideal increment normalized to  $X_{LSB}$ 

$$DNL(k) = \frac{X_{OUT}(k) - X_{OUT}(k-1) - X_{LSB}}{X_{LSB}}$$

# **Differential Nonlinearity (DAC)**

Nonideal DAC



Increment at code k is a signed quantity and will be negative if  $X_{OUT}(k) < X_{OUT}(k-1)$ 

$$DNL(k) = \frac{X_{OUT}(k) - X_{OUT}(k-1) - X_{LSB}}{X_{LSB}}$$
$$DNL = \max_{1 \le k \le N-1} \left\{ |DNL(k)| \right\}$$

DNL=0 for an ideal DAC

# Monotonicity (DAC)

#### Nonideal DAC



Monotone DAC

Non-monotone DAC

Definition:

A DAC is monotone if 
$$\mathcal{X}_{OUT}(k) > \mathcal{X}_{OUT}(k-1)$$
 for all k

Theorem:

A DAC is monotone if DNL(k)> -1 for all k

# **Differential Nonlinearity (DAC)**

Nonideal DAC



Theorem: The INL<sub>k</sub> of a DAC can be obtained from the DNL by the expression  $INL_k = \sum_{i=1}^k DNL(i)$ 

Caution: Be careful about using this theorem to measure the INL since errors in DNL measurement (or simulation) can accumulate

Corollary:  $DNL(k)=INL_{k}-INL_{k-1}$ 

# **Differential Nonlinearity (DAC)**

Nonideal DAC



Theorem: If the INL of a DAC satisfies the relationship

$$|NL < \frac{1}{2} X_{LSB}$$

then the DAC is monotone

Note: This is a necessary but not sufficient condition for monotonicity

## **Differential Nonlinearity (ADC)**

Nonideal ADC



DNL(k) is the code width for code k – ideal code width normalized to  $X_{LSB}$ DNL(k)= $\frac{\chi_{T(k+1)} - \chi_{Tk} - \chi_{LSB}}{\chi_{LSB}}$ 

# **Differential Nonlinearity (ADC)**

**Nonideal ADC** 



 $DNL(k) = \frac{\mathcal{X}_{T(k+1)} - \mathcal{X}_{Tk} - \mathcal{X}_{LSB}}{\mathcal{X}_{LSB}}$  $DNL = \max_{2 \le k \le N-1} \{ |DNL(k)| \}$ 

DNL=0 for an ideal ADC

Note: In some nonideal ADCs, two or more break points could cause transitions to the same code  $C_k$  making the definition of DNL ambiguous

## Monotonicity in an ADC



Definition: An ADC is monotone if the

 $\vec{X}_{OUT}(\mathcal{X}_k) \ge \vec{X}_{OUT}(\mathcal{X}_m)$  whenever  $\mathcal{X}_k \ge \mathcal{X}_m$ 

Note: Have used  $\mathcal{X}_{Bk}$  instead of  $\mathcal{X}_{Tk}$  since more than one transition point to a given code

Note: Some authors do not define monotonicity in an ADC.

## Missing Codes (ADC)



No missing codes

One missing code

Definition: An ADC has no missing codes if there are N-1 transition points and a single LSB code increment occurs at each transition point. If these criteria are not satisfied, we say the ADC has missing code(s).

Note: With this definition, all codes can be present but we still say it has "missing codes"

Note: Some authors claim that missing codes in an ADC are the counterpart to nonmonotonicity in a DAC. This association is questionable.

## Missing Codes (ADC)



# Weird Things Can Happen



- Multiple outputs for given inputs
- All codes present but missing codes

Be careful on definition and measurement of linearity parameters to avoid having weird behavior convolute analysis, simulation or measurements

Most authors (including manufacturers) are sloppy with their definitions of data converter performance parameters and are not robust to some weird operation

**Consider ADC** 



Linearity testing often based upon code density testing

Code density testing:



Ramp or multiple ramps often used for excitation Linearity of test signal is critical (typically 3 or 4 bits more linear than DUT)



• First and last bins generally have many extra counts (and thus no useful information)

• Typically average 16 or 32 hits per code

Code density testing:

$$\overline{C} = \frac{\sum_{i=1}^{N-2} \widehat{C}_i}{N-2}$$

$$DNL_{i} = \frac{\widehat{C}_{i} - \overline{C}}{\overline{C}}$$
$$INL_{i} = \begin{cases} 0 \\ \left[\sum_{k=1}^{i} \widehat{C}_{k}\right] - i\overline{C} \\ \frac{k=1}{\overline{C}} \end{cases}$$

$$\mathsf{DNL} = \max_{1 \le i \le \mathsf{N-2}} \left\{ | \mathit{DNL}_i | \right\}$$

$$\mathsf{INL} = \max_{1 \le i \le N-3} \{ |\mathit{INL}_i| \}$$



i=0,N-2

 $1 \le i \le N-3$ 

- This measurement is widely used
- Does not keep track of order bins are filled
- Some weird things can occasionally happen with this approach



Though INL and DNL for an ADC are rigorously defined, measuring the actual transition points is not practical even if n is small so code density tests are almost always used to "test" the INL and the DNL

#### Performance Characterization of Data Converters

- Static characteristics
  - Resolution
  - Least Significant Bit (LSB)
  - Offset and Gain Errors
  - Absolute Accuracy
  - Relative Accuracy
  - Integral Nonlinearity (INL)
  - Differential Nonlinearity (DNL)
  - Monotonicity (DAC)
  - Missing Codes (ADC)
- Low-f Spurious Free Dynamic Range (SFDR)
- Low-f Total Harmonic Distortion (THD)
  - Effective Number of Bits (ENOB)
  - Power Dissipation

## Linearity

A data converter (ADC or DAC) can be viewed as an amplifier that interfaces between the analog and digital domains

Linearity is of considerable concern in amplifiers irrespective of whether the I/O is analog:analog, analog:digital, digital:analog, or digital:digital

Though INL and DNL give some information about linearity (the term "linearity" is even included in their names!), much information about the actual linearity of a data converter is suppressed in the INL and DNL metrics

The seemingly simple concept of linearity is challenging to accurately characterize

Performance Characterization of Data Converters

- Static characteristics
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  - Least Significant Bit (LSB)
  - Offset and Gain Errors
  - Absolute Accuracy
  - Relative Accuracy
  - Integral Nonlinearity (INL)
  - Differential Nonlinearity (DNL)
  - Monotonicity (DAC)
  - Missing Codes (ADC)
  - ⇒ Low-f Spurious Free Dynamic Range (SFDR)
    - > Low-f Total Harmonic Distortion (THD)
  - Effective Number of Bits (ENOB)
  - Power Dissipation

Linearity Metrics

Spectral Characterization

## INL Often Not a Good Measure of Linearity

Four identical INL with dramatically different linearity



# Linearity Issues

- INL is often not adequate for predicting the linearity performance of a data converter
- Distortion (or lack thereof) is of major concern in many applications
- Distortion is generally characterized in terms of the harmonics that may appear in a waveform when a periodic excitation is applied at the input

## Two Popular Methods of Linearity Characterization

• Integral and Differential Nonlinearity (metrics: INL, DNL)



• Spectral Characterization (Based upon spectral harmonics of sinusoidal signals metrics: THD, SFDR, SDR SNR)





If x(t) is periodic

$$\mathbf{x}(t) = \mathbf{A}_{0} + \sum_{k=1}^{\infty} \mathbf{A}_{k} \sin(k\omega t + \mathbf{\theta}_{k})$$

alternately

$$\mathbf{x(t)} = \mathbf{A}_{0} + \sum_{k=1}^{\infty} \mathbf{a}_{k} \sin(k\omega t) + \sum_{k=1}^{\infty} \mathbf{b}_{k} \cos(k\omega t) \quad \omega = \frac{2\pi}{T}$$
$$\mathbf{A}_{k} = \sqrt{\mathbf{a}_{k}^{2} + \mathbf{b}_{k}^{2}}$$

Termed the Fourier Series Representation of x(t)

Metrics based upon Fourier Series Coefficients Useful for Characterizing how nonlinear a system is !

## Fourier Series Representation of Periodic Continuous-Time Signals

$$\mathbf{x}(t) = \mathbf{A}_0 + \sum_{k=1}^{\infty} \mathbf{A}_k \sin(\mathbf{k}\omega t + \mathbf{\theta}_k)$$

Fourier Series Coefficients Determined From:

$$A_{k} = \frac{1}{\omega T} \left( \int_{t_{1}}^{t_{1}+T} x(t) e^{-jk\omega t} dt + \int_{t_{1}}^{t_{1}+T} x(t) e^{jk\omega t} dt \right)$$

or

$$a_{k} = \frac{2}{\omega T} \int_{t_{1}}^{t_{1}+T} x(t) \sin(kt\omega) dt \qquad b_{k} = \frac{2}{\omega T} \int_{t_{1}}^{t_{1}+T} x(t) \cos(kt\omega) dt$$

Integral is very time consuming, particularly if large number of components are required



Often the system of interest is ideally linear but practically it is weakly nonlinear.

Often the input is nearly periodic and often sinusoidal and in latter case desired output is also sinusoidal

Weak nonlinearity will cause harmonic distortion (often just termed distortion) of signal as it is propagated through the system

Spectral analysis often used to characterize effects of the weak nonlinearity

Spectral Performance Dependent upon Magnitude and Offset of Input



Distortion Types:

#### **Frequency Distortion**

#### Nonlinear Distortion (alt. harmonic distortion)

Frequency Distortion: Amplitude and phase of system is altered but output is linearly related to input (i.e. system remains linear)

Nonlinear Distortion: Characteristic of System that is not linear, frequency components usually appear in the output that are not present in the input

"Distortion" refers to two entirely different phenomenon

Spectral Analysis is the characterization of a system with a periodic input that relates the Fourier series relationships between the input and output waveforms



If 
$$X_{IN}(t) = X_m \sin(\omega t + \theta)$$
  
 $X_{OUT}(t) = A_0 + \sum_{k=1}^{\infty} A_k \sin(k\omega t + \theta_k)$ 

All spectral performance metrics depend upon the sequences  $\langle A_k \rangle_{k=0}^{\infty} = \langle \theta_k \rangle_{k=1}^{\infty}$ 

Spectral performance metrics of interest: SNDR, SDR, THD, SFDR, IMOD

Alternately

$$X_{OUT}(t) = A_0 + \sum_{k=1}^{\infty} a_k \sin(k\omega t) + \sum_{k=1}^{\infty} b_k \cos(k\omega t) \qquad A_k = \sqrt{a_k^2 + b_k^2} \qquad \theta_k = \tan^{-1}\left(\frac{b_k}{a_k}\right)$$

#### **3.3 Fourier Representations** for Four Classes of Signals



There are four distinct Fourier representations, each applicable to a different class of signals. The four classes are defined by the periodicity properties of a signal and whether the signal is continuous or discrete in time. The Fourier series (FS) applies to continuous-time periodic signals, and the discrete-time Fourier series (DIFS) applies to discrete-time periodic signals. Nonperiodic signals have Fourier transform representations. The Fourier transform (FT) applies to a signal that is continuous in time and nonperiodic. The discretetime Fourier transform (DTFT) applies to a signal that is discrete in time and nonperiodic. Table 3.1 illustrates the relationship between the temporal properties of a signal and the appropriate Fourier representation.

#### FS, FT, DTFS, DTFT

**DFT** (**Discrete Fourier Transform**) is a practical version of the **DTFT**, that is computed for a finite-length discrete signal. The **DFT** becomes equal to the **DTFT** as the length of the sample becomes infinite and the **DTFT** converges to the continuous Fourier transform **in the** limit of the sampling frequency going to infinity. Oct 27, 2014

## The DFT is the most important discrete transform, used to perform Fourier analysis in many practical applications.[1] In digital signal processing, the

#### DFS, DTFT, and DFT

Herein we describe the relationship between the Discrete Fourier Series (DFS), Discrete Time Fourier Transform (DTFT), and the Discrete Fourier Transform (DFT). Why? The real reason is that the DFT is easily implemented on a computer and is part of every mathematics package, so it would be nice to know how to determine or approximate the DFT and DTFT on a computer.

#### Fast Fourier transform - Wikipedia

Т

A **fast Fourier transform** (**FFT**) is an algorithm that computes the discrete Fourier transform (DFT) of a sequence, or its inverse (IDFT). Fourier analysis converts a signal from its original domain (often time or space) to a representation in the frequency domain and vice versa.

#### DFT, DFS, FFT, IDFT

# The "Fourier" Representations:

# FS, FT, DTFS,DTFT DFT, DFS, FFT, IDFT

"applies"

"algorithm"

"perform"

"computes"

"converts"

"computed for"

Really fundamental concepts but varying notation and maybe varying perceptions

Assume x(t) is periodic with period T (T=1/f) and band-limited to Mf

x(t) can be expressed in time domain as

$$x(t) = \sum_{i=1}^{M} A_k \sin(k\omega t + \theta_k)$$
 2M parameters  
(A<sub>k</sub>, θ<sub>k</sub>)

IDFT

DFT

Termed the Fourier Series Representation

If x(t) is uniformly sampled 2M times with sampling interval  $T_s$  where  $2MT_s=T$ 

Time domain sequence:  $\vec{x} = \langle x(T_s), x(2T_s), ..., x(2MT_s) \rangle$ 

Denoted as frequency domain sequence:  $\vec{X} = \langle X_1, X_2, ..., X_M \rangle_{(X_k \text{ are complex, } (A_k, \theta_k))}$ 

- Sampling interval not restricted to a single period
- Under certain easily satisfied conditions, x(t) is uniquely represented by X
  <sup>x</sup>(t)=IDFT (DFT(x(t)))

Assume x(t) is periodic with period T (T=1/f) and band-limited to Mf

Fourier Series Representation:

$$x(t) = \sum_{i=1}^{M} A_k \sin(k\omega t + \theta_k)$$
 2M parameters  
(A<sub>k</sub>, θ<sub>k</sub>)

IDFT

Termed DFT

If x(t) is uniformly sampled 2M times with sampling interval T<sub>s</sub> where 2MT<sub>s</sub>=T  $\vec{x} = \langle x(T_s), x(2T_s), ..., x(2MT_s) \rangle$ Time domain sequence:

2M parameters

2M parameters

DFT 2N Denoted as frequency domain sequence:  $\vec{X} = < X_1, X_2, ..., X_M >$ 

$$X_{k} = A_{k}e^{j\theta_{k}}$$

- 2M time domain samples spaced as specified completely characterizes x(t) for all t ٠
- Frequency domain sequence  $\vec{X}$  completely characterizes x(t) for all t •

Will focus on how Fourier Series Representation of a periodic signal is altered when it passes through a weakly nonlinear system

Relationship between DFT and continuous-time Fourier Series representation is fundamental to characterizing spectral performance of a weakly nonlinear system





Often <u>ideal</u> response will have only fundamental present and all remaining spectral terms will vanish



For a low distortion signal, the 2<sup>nd</sup> and higher harmonics are generally much smaller than the fundamental

The magnitude of the harmonics generally decrease rapidly with k for low distortion signals



where  $\langle A_k \rangle_{k=0}^{\infty}$  are the Fourier series coefficients of f(t)

Total Harmonic Distortion, THD

 $THD = \frac{RMS \text{ voltage in harmonics}}{RMS \text{ voltage of fundamental}}$ 

$$THD = \frac{\sqrt{\left(\frac{A_2}{\sqrt{2}}\right)^2 + \left(\frac{A_3}{\sqrt{2}}\right)^2 + \left(\frac{A_4}{\sqrt{2}}\right)^2 + \dots}}{\frac{A_1}{\sqrt{2}}}$$
$$THD = \frac{\sqrt{\sum_{k=2}^{\infty} A_k^2}}{A_1}$$

Spurious Free Dynamic Range, SFDR

The SFDR is the difference between the fundamental and the largest harmonic



SFDR is usually determined by either the second or third harmonic

In a fully differential symmetric circuit, all even harmonics are absent in the differential output !



**Theorem:** In a fully differential symmetric circuit, all even-order terms are absent in the Taylor's series output for symmetric differential excitations !

![](_page_43_Figure_2.jpeg)

Proof: Expanding in a Taylor's series around  $V_{ID}=0$ , we obtain

$$V_{01} = x(V_{1D}) = \sum_{k=0}^{\infty} h_k (V_{1D})^k \qquad V_{0D} = V_{01} - V_{02} = \sum_{k=0}^{\infty} h_k (V_{1D})^k - \sum_{k=0}^{\infty} h_k (-V_{1D})^k V_{02} = x(-V_{1D}) = \sum_{k=0}^{\infty} h_k (-V_{1D})^k \qquad V_{0D} = \sum_{k=0}^{\infty} h_k [(V_{1D})^k - (-V_{1D})^k] V_{0D} = \sum_{k=0}^{\infty} h_k [(V_{1D})^k - (-1)^k (V_{1D})^k]$$

When k is even, term in [] vanishes

**Theorem:** In a fully differential symmetric circuit, all even harmonics are absent in the differential output for symmetric differential excitations !

![](_page_44_Figure_2.jpeg)

where  $h_k$ ,  $g_k$ , and  $\theta_k$  are constants

That is, odd powers of sin<sup>n</sup>(x) have only odd harmonics present and even powers have only even harmonics present

![](_page_45_Picture_0.jpeg)

## Stay Safe and Stay Healthy !

## End of Lecture 28